




**BHARATI VIDYAPEETH'S  
COLLEGE OF ENGINEERING, KOLHAPUR**


**Department of Electronics and Telecommunications Engineering**


**Staff Details for CAY 2017-18**


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	20	1	
<b>Area of Specialization</b>	Communication Engineering, Wireless Engineering		


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	18	-	
<b>Area of Specialization</b>	Digital Electronics, Image Processing		


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	14	-	
<b>Area of Specialization</b>	VLSI Design, Analog Design		


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	13	1	
<b>Area of Specialization</b>	Embedded System, Cryptography and Network Security		


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	10	-	
<b>Area of Specialization</b>	Image Processing and VLSI Design		


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	6	-	
<b>Area of Specialization</b>	Analog Circuit, Signal Processing.		


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	5	-	
<b>Area of Specialization</b>	Communication and Network		


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	5	0	
<b>Area of Specialization</b>	Digital Electronics, Microwave Engineering		


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	4	14	
<b>Area of Specialization</b>	Industrial Automation, Power Electronics		


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	9	-	
<b>Area of Specialization</b>	Image Processing, Electromagnetic Engineering		


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	8	-	
<b>Area of Specialization</b>	Embedded System , VLSI System		


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	9	1	
<b>Area of Specialization</b>	Digital Communication, Signal Processing		


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	6	1.5	
<b>Area of Specialization</b>	Power Electronics, Instrumentation		


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	7	-	
<b>Area of Specialization</b>	Embedded System, VLSI Design		


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	4	-	
<b>Area of Specialization</b>	Image Processing, Embedded System		

<b>Name</b>	<b>Prof. Vishal M. Patil</b>		
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	3	-	
<b>Area of Specialization</b>	VLSI Design		

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	-	-	
<b>Area of Specialization</b>	Communication		

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	-	6 Months	
<b>Area of Specialization</b>	Communication		

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	3	-	
<b>Area of Specialization</b>	Image Processing, Analog Design		

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	-	-	
<b>Area of Specialization</b>	Image Processing,		